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IN THE U.S. PATENT & TRADEMARK OFFICE

Applicant: Jonathan Y. Chen : GROUP ART UNIT 2152
Serial No.: 09/824,389 : Examiner:
Filed: April 2, 2001 : May 29, 2003
Title: A Method and System of : Lynn L. Augspurger
Automatic Delay Detection and Receiver: IBM Corporation
Adjustment for Synchronous Bus : 2455 South Road, P386
Interface : Poughkeepsie, NY 12601

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. box 1450
Alexandria, VA 22313-1450

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JUN 04 2003

Technology Center 2100

Sir:

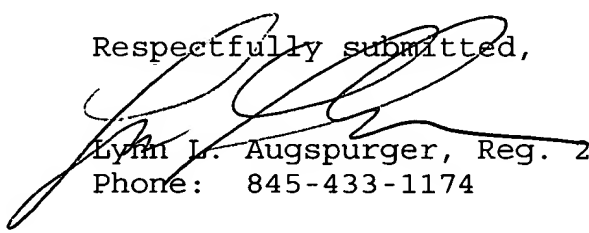
The Examiner's attention is directed to the enclosed references which were included in a PCT Search Report dated April 29, 2003 (copy enclosed). The relevant portions of the references are indicated in the search report.

A form PTO-1449 listing the references and copies of the references are enclosed.

CERTIFICATION UNDER 37 C.F.R. 1.97(e)

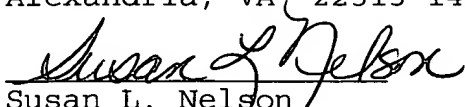
Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

Respectfully submitted,


Lynn L. Augspurger, Reg. 24,227
Phone: 845-433-1174

CERTIFICATE OF MAILING UNDER 37 C.F.R. 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-1450 on May 29, 2003.


Susan L. Nelson

5/29/2003
Date of Signature

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